

**BALL GRID ARRAY ATTACHING MEANS HAVING IMPROVED
RELIABILITY AND METHOD OF MANUFACTURING SAME**

RELATED APPLICATION DATA

- [001] This application is a Continuation-In-Part of U.S. Patent Application Serial No. 09/954,026, filed September 18, 2001, by Sang-Young KIM, et al., entitled "Hollow Solder Structure Having Improved Reliability and Method of Manufacturing Same."

BACKGROUND OF THE INVENTION

1. Field of the Invention

- [002] The present invention relates to an electronic device having a Ball Grid Array (BGA) solder mounting system and, more particularly, to a BGA system having an array of hollow solder balls and a method for manufacturing the same.

2. Description of the Related Art

- [003] A common mounting and electrical-connection mechanism used in integrated circuits (ICs) is a ball grid array (BGA), wherein small solder balls are placed and retained at each one of a multitude of connection pads of the IC for solder connection to a mounting substrate or planar surface having opposing metal connecting pads. When heat is applied, the solder balls liquefy and flow via a fluxing agent included in the solder compound over any exposed metal surfaces of the connection pads, thereby forming a reliable electrical connection with each mounting pad. After cooling, the

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hardened solder additionally provides a rigid mounting structure for mechanically retaining the IC to the substrate or planar surface.

[004] Typically, the size of the mounting package for the IC can be reduced to the size of a semiconductor chip, thus creating a Chip Size Package or Chip Scale Package (CSP). In the CSP, unlike conventional periphery-leaded (i.e. wire-bonded) packages, an array of external terminals and the BGA solder balls are distributed over the surface of the IC to directly interconnect the package to a printed circuit board (PCB). After processing, the resulting solder structures are generally inelastic, and provide a solid mounting mechanism for the assembly.

[005] Disadvantageously, since the material composition of the chip and the opposing epoxy-glass material of a conventional PCB can have widely mismatched Coefficient of Thermal Expansion (CTE), any thermal cycling effects, such as those normally associated with turn-on and turn-off of related circuitry, can produce differing expansion movement of the opposing planar surfaces. This movement produces lateral shearing stresses on the solder joint, which is absorbed by the solder balls, more specifically by the junction of the solder ball and the metal connecting pads. With repeated thermal cycling, metal fatigue at this junction can cause the solder structure to crack and fail, rendering an entire circuit board inoperable. In other words, when the chip heats up during use, both the chip and the board expand, and when the heat is removed, both the chip and the substrate shrink. The problem that arises is that the chip and the substrate expand

and contract at different rates according the CTE, thereby stressing the interconnections or solder balls connecting them.

[006] Fig. 1 illustrates a cross-sectional view of a conventional ball grid array (BGA) interface structure 10 having a multitude of solid solder balls 12. Structure 10 consists of upper planar element 14 having an array of first metal conductor pads 16 rigidly connected via a plurality of solder balls 12 to an array of lower planar elements 18 having a plurality of second metal conductor pads 20 so as to provide both electrical and mechanical connection between electronic circuits on each of the planar elements. Conductors are separated from lateral neighboring conductors of the array by isolation spaces 22 appropriately located on each planar element. Each of planar elements 14 and 18 can have a different CTE. However, an excessive disparity between the CTEs can produce thermal cycling failure in the form of cracking of the rigid solder joints as previously discussed.

[007] Occasionally, conventional out-gassing of vaporized flux is not completed due to process and/or solder compound irregularities, such as insufficient wetting of the solder compound, and results in small voids being located at the conductor-solder interfaces. Such voids create an area of fatigue weakness in the resulting joining structures.

[008] Fig. 2 illustrates a cross-sectional area of a conventional BGA structure, wherein small voids 24 due to insufficient conventional wetting are shown at the junction of one or more of the plurality of conductor pads 16 and solder balls 12 in the array. Such voids 24 are formed when each of

the molten solder balls 12 wets on the associated conductor pad 16, and the flux in the solder paste compound flows outward from the center of the conductor pad 16. As the temperature of the solder compound rises further, the flux is vaporized and a major portion of the flux vapor is dissipated into the atmosphere. However, minor portion of this vapor remains trapped in solder ball 12 as it is cooled and forms the small voids 24 inside the solder ball 12.

[009] The configurations shown in Figs. 1 and 2 are susceptible to cracking at each joint connecting the rigid conductor pad 16 and the rigid solder ball 12 under application of opposing lateral forces on the planar elements 14 and 18. Such a failure of both the electrical connection and the mechanical mounting mechanism has heretofore precluded the use of epoxy-glass as a reliable substrate material for BGA chip attachment applications, in favor of a more expensive ceramic material which has a CTE closer to that of the chip. Similarly, attachment of ceramic carrier modules, such as hybrids and CSPs, to an epoxy-glass printed circuit board is precluded for the same reason. A detailed joint interface of solder balls used in a conventional BGA type semiconductor device mounting construction is disclosed, for example, in U.S. Patent No. 6,122,177 and PAJ No. 1998-209591. Detailed manufacturing assembly technical reports from International Business Machines Corporation, entitled "Doubled-Sided 4Mb SRAM Coupled Cap PBGA Card Assembly Guide," and from Texas Instrument Corporation,

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entitled "MicroStar BGA Packaging Reference Guide," would also be beneficial to the reader.

[010] The prior art addresses the presence of these small voids in the solder balls and a resulting joint embrittlement as significant problems. A small void in this context is defined as a gaseous volumetric displacement within the interior of a solder ball due to thermal expansion (i.e. boiling) of low-temperature solder flux solvents, since such gas material will remain trapped within a cooled solder structure. Conventional solder processing typically incorporates a warm-up period to allow time for de-gassing of such solvents, thereby minimizing such voids to yield a recommended finished total gaseous volume of less than 0.1% of the total solder structure volume. Disadvantageously, such heating can prematurely dry the solder paste included in the solder ball, leading to degraded electrical connections.

[011] Thus, conventional BGA structures are not sufficient to prevent solder cracking or the breakage of solder ball interconnection, especially when used with a chip and an epoxy-glass substrate. Therefore, what is needed is a newly designed CSP with improved interconnection reliability, especially between the chip and the PCB, and a method of manufacturing of the same.

SUMMARY OF THE INVENTION

[012] In a preferred embodiment of the present invention, improved reliability of an array of solder ball connections in a ball grid array (BGA) semiconductor device may be attained using an array of resilient solder

bonding structures, each structure comprising a solder ball having a curved exterior surface enclosing an interior cavity, with each structure connecting a plurality of conductive contact pads on a first planar surface to a corresponding plurality of conductive contact pads on a second planar surface. The presence of such interior cavities produces a plurality of thin-walled solder structures that have sufficient resiliency and flexibility to “bend” under laterally applied stresses, such as movement of the first planar surface relative to the second planar surface during thermal expansion or vibration, thereby removing stress vectors that would occur at each one of a plurality of joints between the conductive contact pads and the solder ball.

[013] The interior cavity of each one of the array of resilient solder bonding structures may comprise a volume that equals from 1% to 90% of the exterior volume of the melted and cooled solder ball. Such interior cavities may be controllably formed via a seeding catalyst preferably in the form of an annular ring in contact with a solder ball having a volatile fluxing agent. The conductive arrays may be formed on contact pads having annular, circular, or other geometric patterns that may be conducive to forming such interior cavities during the melting and subsequent cooling of the solder balls. Opposing contact pads of the individual planar substrates may be identical or different.

[014] According to the preferred embodiment of the present invention, a connection system comprises a resilient ball grid array (BGA) electrical and mechanical attachment means for attaching a first planar substrate to a

second planar substrate. The connection system comprises a plurality of solder ball structures, each solder ball structure having a curved exterior surface enclosing a first volume and having an interior cavity having a displacement constituting a second volume, each solder ball structure attaching one of a plurality of first metal contacts on the first planar substrate to an associated one of a plurality of second metal contacts on the second planar substrate.

[015] The first and second planar substrates may comprise a first and second printed circuit board (PCB) being joined together, wherein the first and/or second printed circuit boards may be made of organic or inorganic materials. The first and second planar substrates may individually or together be comprised of one or more materials from the group consisting of plastic, ceramic, and epoxy-glass. The first and second planar substrates may be comprised of materials and forms that may be characterized as being an integrated circuit (IC), an IC carrier, a leadframe, a generally rigid substrate, or a flexible substrate. The first and second surfaces may have the same or different properties. For example, in addition to a chip-to-PCB and a PCB-to-PCB connection array, various other interconnections may be made, such as ceramic carrier to PCB, chip to ceramic carrier, or plastic module substrate to PCB or other plastic module substrate.

[016] These and other features of the present invention will be readily apparent to those of ordinary skill in the art upon review of the detailed description that follows.

BRIEF DESCRIPTION OF THE DRAWINGS

- [017] Fig. 1 illustrates a cross-sectional view of a conventional ball grid array (BGA) interface structure having a multitude of solid solder balls.
- [018] Fig. 2 illustrates a cross-sectional area of a conventional BGA structure, wherein small voids due to insufficient conventional wetting are shown at the junction of conductor pad and solder ball.
- [019] Fig. 3 illustrates a cross-sectional view of a barrel-shaped solder structure featuring a large void according to a preferred embodiment of the present invention.
- [020] Fig. 4 illustrates a cross-sectional view of the solder structure shown in Fig. 3 under conditions where lateral stress forces are applied to the planar elements in directions that tend to move planar elements in opposite directions.
- [021] Fig. 5 illustrates a cross-sectional view of a solder structure according to an alternative embodiment of the present invention wherein one planar element has an annular conductor pad and the other planar element has a circular conductor pad.
- [022] Fig. 6 illustrates the bending motion in responses to lateral stress forces similar to those shown in Fig. 4.

DETAILED DESCRIPTION OF THE INVENTION

- [023] The present invention will now be described more fully with reference to the accompanying drawings, in which preferred embodiments of the invention are shown. This invention may, however, be embodied in many

different forms and should not be construed as being limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the concept of the invention to those skilled in the art. In the drawings, the shapes and thickness of elements may be exaggerated for clarity, and the same reference numerals appearing in different drawings represent the same element.

[024] Fig. 3 illustrates a cross-sectional view of an array of barrel-shaped solder structures 26, each featuring a central cavity 28 according to a preferred embodiment of the present invention. Through the use of a plurality of annular conductor pads 30 configured as an array on upper planar element 14, a plurality of first “seeding” points may be created by the accumulation of vaporized flux at the non-conducting/non-wetting centers of the annuli. By including an opposing array of annular conductor pads 32 on lower planar element 18, a plurality of second “seeding” points may be created.

[025] After placement of a plurality of solder balls at the center of each annular conductor pad 32 and retention preferably using an adhesive, such as a tape, upper planar element 14 is preferably placed and retained on the plurality of high-volatility solder balls, such that associated annular conductor pads are aligned. The assembly is then heated to bring the plurality of high-volatility solder balls to the melting point of the solder. As the vapor expands during solder heating and liquefaction, a first interior

cavity is formed at each array location on upper planar element 14 that is contained by the surface tension and viscosity of the molten solder. The first interior cavity joins with a second interior cavity similarly formed on lower planar element 18 to produce the resulting interior cavity 28. In the molten solder state, the upper planar element 14 will float on the liquefied solder balls, causing a natural aligning of the opposing conductor pads 30 and 32 due to surface tension equilibrium of the plurality of liquefied solder balls.

[026] The surface tension properties also force the formation of an exterior convex shape on the liquefied structure, that when cooled, solidifies in the barrel-shaped form of Fig. 3, due to solidification of the outer shell before the vaporized fluxing agent in the interior can contract.

[027] During heating, the solder compound does not wet at hole areas 34 and 36 of annular conductor pads 30 and 32 under an epoxy resin (or polyimide tape) because the contact angle is 180° between the epoxy resin (or polyimide tape) and solder ball 26. Exemplary land holes 34 and 36 are approximately 0.1mm diameter. The small voids surrounding the land holes 34 and 36 merge and become concentrated, and the large void 28 is initiated at these point. Relative volumes of the cavity can range from 1% to 90% of the total volume of the solder structure. The volume of the cavity may be controlled by controlling the temperature and time during heating of the solder. The composition of the solder should balance the properties of the solder and solder-alloys with the properties of a fluxing vapor. Specifically, an

exemplary solder compound can consist of portions any of the general soldering materials, such as solder, silver, and tin, and a fluxing agent, such as one or more from the group of rosin, resin, activator, thixotropic agent, and a high temperature boiling solvent.

[028] Fig. 4 illustrates a cross-sectional view of the array of solder structures shown in Fig. 3 under conditions of lateral stress forces being applied in directions that tends to move planar elements 14 and 18 in opposite directions. The skew of the plurality of barrel-shaped solder structures 26 illustrates the bending motion of the flexible walls of solder structures 26 such that potentially destructive stress forces are neutralized and reduced or removed from junction areas 38 and 40.

[029] Fig. 5 illustrates an array of solder structures 42 according to an alternative embodiment of the present invention, wherein planar element 14 has a plurality of circular conductor pads 44, and planar element 18 has a plurality of annular conductor pads 46. This arrangement creates at each array location a solder structure 48 having a circular or tear-drop shape, that still retains the thin flexible walls 50. The plurality of annular conductor pads can be located on either upper planar element 14 or lower planar element 18, with the plurality of circular conductor pads on the opposing surface with the result that there is an attendant effect that locates the cavity coincident with each hole of the annulus.

[030] Fig. 6 illustrates the bending motion of the array of solder structures shown in Fig. 5 in response to lateral stress forces similar to those shown in

Fig. 4. Similar to the structure shown in Fig. 4, the potentially destructive stress forces are neutralized or absorbed by the flexible walls of the solder structure and reduced or eliminated at the junctions 44 and 46 between solder ball 48 and the planar elements 14 and 18, respectively. Although the solder structure of Fig. 5 is shown to have thicker walls, manipulation of the solder composition and applied thermal parameters, including both variations of temperature and time, can produce a wall thickness of any desired dimension.

[031] The size of the resulting cavity 28 is dependent on several physical and chemical factors, including without limitation: flux-solder weight ratios composing the solder compound; type and properties of the solder materials included in the solder compound; type and relative vaporization or boiling point of the fluxing agent used; relative size of the central hole in the annular ring 34 and 36 to the diameter of the annulus 30 and 32; relative size of the annulus versus the initial diameter of the spherical solder ball 26 and 48; relative separation distance of planar elements 14 and 18 versus the diameter of the spherical solder ball 26 and 48; and rates, magnitudes, and dwell times of temperature changes on the assembly. Further, different qualitative measurements can be used. For example, although mixing the solder compound by weight is a convenient measure for determining relative portions of each element, volumetric measure can also be used. Determination of a resultant solder structure can also be addressed as a ratio of gas versus solder volumes, wall thickness versus total diameter of

the structure width dimension, or cavity diameter versus planar separation distance, and still be within the scope of the present invention.

[032] Thus, with the preferred embodiments of the present invention, the stresses applied to the various joints can be significantly dissipated or reduced. Accordingly, interconnection reliability can be significantly improved in accordance with the present invention. With the embodiments of the present invention, ceramic carrier modules having arrays of connecting pads, such as hybrids and CSPs, can be reliably attached to an opposing array of contact pads on an epoxy-glass printed circuit board. Similarly, such resilient bonding arrays allow for the bonding of PCB-to-PCB in such a way that a directional "grain" of the PCBs is not relevant. Further, bonding of chips to wafers, chips to chips, etc can all benefit from such a resilient bonding array. The resiliency benefits are not restricted to neutralizing thermal expansion forces, but have applicability in environments having degrading mechanical forces, such as motor vibrations, for example.

[033] Further, the invention is not restricted to IC-to-IC or substrate, nor IC-to-PCB, but any variation and type of semi-rigid planar to planar interface the require resilient mechanical and electrical connection. Such planar interfaces may or may not have differing CTEs to derive the benefits of the resilient bonding structure. Thus, the flexibility afforded by the embodiments of the present invention provides improved reliability in adverse thermal and mechanical environments.

[034] A preferred embodiment of the present invention has been disclosed herein and, although specific terms are employed, they are used in a generic and descriptive sense only and not for purpose of limitation. Accordingly, it will be understood by those of ordinary skill in the art that various changes in form and details may be made without departing from the spirit and scope of the invention as set forth in the following claims.

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